

This listing of claims will replace all prior versions, and listing, of claims in the application:

Listing of Claims:

Claims 1-41 (canceled).

Claim 42 (currently amended): An apparatus that outputs multiple video signals, comprising:

first signal processing components, wherein the first signal processing components process a first video signal, the first signal processing components comprising:

a first tuner;

a first demodulator coupled to the first tuner;

a first demultiplexor coupled to the first demodulator;

a first video/graphics/text demultiplexor coupled to the first demultiplexor,

wherein the first video/graphics/text demultiplexor separates graphics, video and text from the first video signal; and

a first video decompressor coupled to the first video/graphics/text demultiplexor;

and

second signal processing components, wherein the second signal processing components process a second video signal, the second signal processing components also operable to scale and redirect the second video signal, the second signal processing components comprising:

a second tuner;

a second demodulator coupled to the second tuner;

a second demultiplexor coupled to the second demodulator;

a second video/graphics/text demultiplexor coupled to the second demultiplexor,

wherein the second video/graphics/text demultiplexor separates graphics, video and text from the second video signal; and

a second video decompressor coupled to the second video/graphics/text demultiplexor, wherein at least the second signal processing components are located on an upgrade card insertable into an existing set top terminal to provide digital picture-on-picture capability.

Claim 43 (original): The apparatus of claim 42, further comprising a first decryptor and a first memory in the first signal path and a second decryptor and a second memory in the second signal path.

Claims 44-45 (canceled).

Claim 46 (previously amended): The apparatus of claim 42, further comprising a video combiner, the video combiner combining a processed, scaled and repositioned first signal and a processed second video signal for display, wherein the processed, scaled and repositioned first video signal is overlaid on the processed second video signal.

Claim 47 (original): The apparatus of claim 46, wherein a size of the overlaid first video signal is smaller than a size of the processed second video signal.

Claim 48 (previously amended): The apparatus of claim 42, further comprising a video combiner, the video combiner combining a processed, scaled and repositioned first signal and a processed second video signal for display, wherein the processed second video signal is scaled and repositioned, and wherein the processed, scaled and repositioned first and second video signals are displayed in a split screen format.

Claim 49 (previously amended): The apparatus of claim 42, further comprising a third signal path having third signal processing components, wherein the third signal processing components process a third video signal, the third signal processing components also operable to scale and redirect the third video signal, wherein the third signal processing components are substantially identical to the first and the second signal processing components, and wherein the first, second and third video signals are displayed simultaneously on a display.

Claim 50 (original): The apparatus of claim 49, wherein the first and the third video signals are overlaid on the second video signal.

Claim 51 (original): The apparatus of claim 49, wherein the first, second and third video signals are displayed in a split screen format.

Claim 52 (original): The apparatus of claim 49, further comprising:

- audio decompressors in the first, second and third signal paths; and
- a switch coupled to the audio decompressors, wherein the audio decompressors provide audio signal associated with video signals, and wherein the switch operates to select an audio output from one of the audio decompressors.

Claim 53 (previously amended): The apparatus of claim 42, wherein processed, scaled and redirected video signals are provided as digital signals for display on a digital television.

Claim 54 (previously amended): The apparatus of claim 42, wherein the first signal path includes a first NTSC encoder and the second signal path includes a second NTSC encoder, the

first and the second NTSC encoders operable to convert processed video signals and processed, scaled, repositioned video signals to analog format for display on an analog television.

Claims 55-56 (canceled).

Claim 57 (previously amended): The apparatus of claim 42, wherein the first video signal is provided to a first television and the second video signal is provided to a second television.

Claims 58-59 (cancelled).

Claim 60 (previously amended): The apparatus of claim 42, wherein the first signal path further includes first audio processing components and the second signal path further includes second audio processing components.

Claim 61 (previously amended): The apparatus of claim 42, further comprising a switch for selecting a first or a second audio signal for output, the switch activated by operation of a remote control.

Claim 62 (original): The apparatus of claim 61, wherein the first and the second audio signals may be provided in multiple languages, and wherein a menu of available languages is present on screen for selection of a desired language.

Claim 63 (original): The apparatus of claim 61, wherein the first and the second audio signals are associated with the first video signal.

Claim 64 (previously amended): The apparatus of claim 61, wherein the switch is operable to select additional audio signals for output, the additional audio signals associated with the second video signal.

Claims 65-72 (canceled).

Claim 73 (previously added): The apparatus of claim 42, wherein the first signal processing components are located on the upgrade card.

Claim 74 (previously added): The apparatus of claim 42, further comprising a microprocessor that controls processing on the first and the second signal paths such that the first video signal and the second video signal are displayed on one or more displays, and wherein the first signal processing components and the second signal processing components are substantially identical.

Claims 75-76 (canceled).